more. Relays are electromechanical devices with certain challenges in their application. One major challenge is the arcing between two switch contacts, an electric discharge caused by the voltage across them and sustained by the current running through them. Arcing leads to a shortened lifetime or worst case to destruction if the contacts are welded together. Relay providers have several solutions to overcome this problem, such as a capacitor across the load, gas filled chambers, etc. The temperature range from HV DC relays is often limited to -40°C to 85°C and the switching speed is in the

range of several tens of milliseconds.

Figure 2: Block schematic

High-Voltage Battery Disconnect Switch

This article is a short introduction to a high-voltage battery disconnect switch, where transistors have replaced the old-fashioned relays. It briefly explains why clients should choose this solution from VisIC Technologies, a provider of innovative Gallium Nitride Transistors.

By Bernd Schmoelzer, Field Application Engineer, VisIC Technologies

Electric cars currently on the market use 400 and 800 V batteries with nominal currents above 200 amperes, which could be lethal for living beings if this high voltage and current were connected to the chassis or any conductive part of the car. To prevent such a scenario, manufacturers use high voltage and high current directcurrent contactor relays, disconnecting the battery plus and minus rail from the high-voltage-board net shown in Figure 1.

Alternative to relays is bi-directional solid state semiconductor switches, described below. It focuses on the main contractor, aware that auxiliary circuits also need these switches.

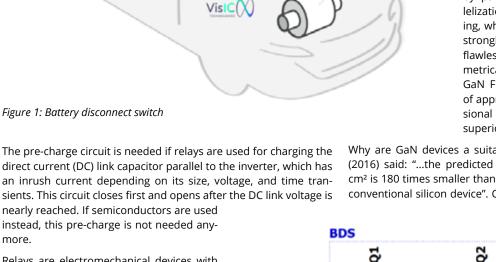
Implementing semiconductors as relay replacement is done in such a way that two transistors are placed in anti-series to block the current in both directions (see Figure 2 below), with a n-channel MOSFET. Alternatively, every other type of FET could be gener-

> ally used. VisIC core competence is in wide bandgap Gallium Nitride (GaN) FETs in a Direct Drive Configuration, hence the reason for proposing them for the HV-BDS.

> requirements What are the for the FETs used in the BDS? During normal operation the switch is constantly on, therefore RDS_{on} is a dominant parameter, defining the conduction losses (P_{con}=I²*RDS_{on}). A desired minimum can be achieved by the technology itself and by parallelization of multiple dies. Parallelization is critical for proper current sharing, which must be guaranteed. It depends strongly, among other parameters, on a flawlessly printed circuit layout with symmetrical stray inductances. Depletion mode GaN FETs provides high electron mobility of approx. 1500 cm²/V*s from the 2-dimensional electron gas (2DEG) combined with superior reliability.

> > Load

Why are GaN devices a suitable candidate for a HV-BDS? Baliga (2016) said: "...the predicted specific on-resistance of 0.4mohm/ cm² is 180 times smaller than the ideal specific on-resistance for a conventional silicon device". Commercial devices are currently not

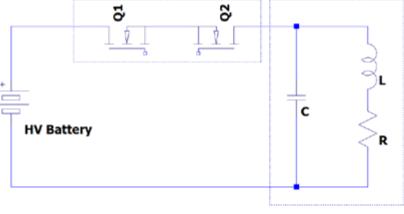


HV-BDS

Battery

Inverter

E-Engine



at this prediction, but even if the resistance is double, it would be 90 times smaller than a silicon switch. Due to that, GaN transistors can be made either much smaller for the same RDS_{on} or have much less resistance for the same size and are a perfect fit for battery dis-

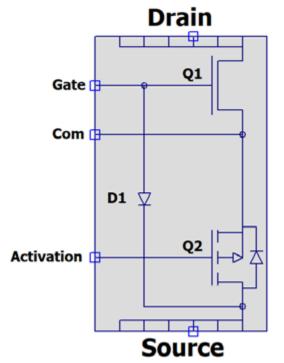


Figure 3: Direct Drive GaN

connect switches. VisIC's direct drive configuration, set out in Figure 3, shows how to control the GaN device, producing multiple benefits compared to other solutions on the market, e.g., no reverse recovery losses, increased reliability, etc.

What are the challenges facing Gallium Nitride in this application? Lateral GaN FETs do not have any avalanche breakdown tolerance (Baliga). VisIC switches have therefore a high enough breakdown voltage margin. The 650 V rated devices have a static blocking voltage above 1600 V providing robustness to surge and overvoltage tested by Q. Song et.al. (2022) at Virginia Tech. The dynamic breakdown voltage is even more than 2 kV. In a short circuit event, the device must withstand the high currents running through the channel. Song showed that the 22 mOhm device from VisIC can handle repetitive 358A for 5 microseconds. Beside this technological solution, a discrete approach can be implemented protecting the FET in a short circuit event within 100 ns. The detailed explanation can be found in the Short Circuit Protection Application Note APN-01650-0003 Rev1.0.

References:

- 1. Baliga (2016): Gallium Nitride and Silicon Carbide Power Devices, World Scientific
- 2. VisIC Technologies Short Circuit Protection Application Note APN-01650-0003 Rev1.0
- Q. Song et al., "Evaluation of 650V, 100A Direct-Drive GaN Power Switch for Electric Vehicle Powertrain Applications," 2021 IEEE 8th Workshop on Wide Bandgap Power Devices and Applications (WiPDA), 2021, pp. 28-33, doi: 10.1109/ WiPDA49284.2021.9645143.

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